

**REMARKS**

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The Office Action dated June 12, 2008 has been received and its contents carefully reviewed.

Claims 1, 5, 6 and 13 are hereby amended, claims 4, 9, 10 and 11 are hereby cancelled, and claims 3, 8, 12 and 16-22 were previously cancelled. Accordingly, claims 1, 2, 5-7, 9-11, 13 and 15 are currently pending. Reexamination and reconsideration of the pending claims are respectfully requested.

In the Office Action, Claims 1, 2, 9-11, 13 and 15 are rejected under 35 U.S.C. 102(b) as being anticipated by Suzuki (U.S. Patent No. 6,369,786, hereinafter, referred as Suzuki), and claims 5-7 are rejected under 35 U.S.C. 103(a) as being unpatentable over Suzuki in view of Ishizuka et al. (U.S. Patent No. 6,756,951, hereinafter, referred as Ishizuka), and claims 4 and 14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Suzuki in view of Ha et al. (U.S. Patent No. 7,030,842, hereinafter referred as Ha)

The rejection of claims 1, 2, 5-7, 9-11, 13 and 15 are respectfully traversed and reconsideration is requested.

Applicants respectfully submit that claims 1 and 13 are patentable over Suzuki. Claim 1 recites an electro-luminescence display device having a combination of elements including, for example, "a gamma voltage driver that generates a plurality of gamma voltage signals corresponding to image data; and a plurality of data driving circuits that apply the plurality of gamma voltage signals to the pixel cells along a data line during a first time of within the horizontal period and applying current signals corresponding the plurality of gamma voltage signals to the pixel cells during a second time within the horizontal period after the first time of the horizontal period, wherein each of the plurality of data driving circuits includes a voltage driver that applies the plurality of gamma voltage signals corresponding to image data to the data lines to pre-charge the plurality of gamma voltage signals onto storage capacitors in the pixel cells, and a current driver that allows the current signals corresponding to the plurality of gamma voltage signals image data to flow into the pixel cells". Claim 13 recites a method of driving an electro-luminescence display device having a combination of elements including, for example, "applying a plurality of gamma voltage value corresponding to image data from a voltage driver to the data lines during a first time of within the horizontal period to pre-charge the plurality of

gamma voltage value onto a storage capacitors of the pixel cells; and applying a current value signals corresponding to the image data plurality of gamma voltage value to the data lines during a second time within the horizontal period after the first time". None of Suzuki, Ishizuka and Ha teaches, either expressly or inherently, at least these features of the claimed invention.

The Examiner asserted that Suzuki discloses a plurality of data driving circuits (i.e. each of the current and voltage supply CS and C components in 2 and 3) that apply voltage signal to the pixel cells along a data line during a first time (T1) of within the horizontal period and apply current signals to the pixel cells during a second time (T2) within the horizontal period after the first time of the horizontal period (i.e. the first time is for precharging voltages T1 and the second period is of current driving period T2), and Ha teaches a gamma voltage driver (46) that applies a plurality of gamma voltage levels to the voltage driver (44) so as to generate the voltage signal.

However, the precharge circuit (3) of the data driving circuit in Suzuki comprises select switches C1 to Cx connected to the signal electrodes SiE1 to SiEx, and power sources 5 to supply power to the signal electrodes SiE via the and the selected switches C1 to Cx. And the power sources 5 provide the signal electrodes SiE1 to SiEx with the threshold voltage  $V_t$  at which the organic EL starts emitting light, via the selected switches C1 to Cx. That is, the precharge circuit (3) of Suzuki provides the signal electrodes (data lines) SiE1 to SiEx with a same voltage  $V_t$  as shown in Fig. 4.

On the other hand, the voltage driver of claimed invention applies the plurality of gamma voltage signals corresponding to image data to the data lines to pre-charge the plurality of gamma voltage signals onto storage capacitors in the pixel cells. That is, the voltage driver of claimed invention provides the data lines with different voltages.

Accordingly, there is a difference in that the precharged voltages of claimed invention are different from in each data line because the gamma voltage signals corresponding to image data are supplied to the data lines, but the precharged voltages of Suzuki are same in all the data lines because the same voltage  $V_t$  is supplied to the data lines.

Furthermore, the gamma voltage driver of the claimed invention generates a plurality of gamma voltage signals corresponding to image data and supplies the gamma voltage signals to the voltage driver (precharge circuit) and the current driver. However, the gamma voltage generator of Ha applies a plurality of gamma voltages to D/A converter of data driver.

Accordingly, the combination of the Ha and Suzuki do not teach or suggest the above mentioned features of claimed invention because none of Suzuki, Ishizuka and Ha teaches, either expressly or inherently, at least these features of the claimed invention.

As Applicants have presented above, claims 1 and 13 are allowable over Suzuki, Ishizuka and Ha. Applicants respectfully submit that claims 2, 5-7, 9-11 and 15 are patentable over Suzuki, Ishizuka and Ha by virtue of dependency from claims 1 or 13.

Applicants believe the foregoing amendments place the application in condition for allowance and early, favorable action is respectfully solicited.

If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. 1.136, and any additional fees required under 37 C.F.R. 1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911.

Application No.: 10/825,365

Docket No.: 8733.1032.00-US

Amendment dated September 10, 2008

Response to Office Action dated June 12, 2008

Please credit any overpayment to deposit Account No. 50-0911.

Dated: September 10, 2008

Respectfully submitted,

By \_\_\_\_\_  
Eric J. Nuss

Registration No.: 40,106

McKENNA LONG & ALDRIDGE LLP

1900 K Street, N.W.

Washington, DC 20006

(202) 496-7500

Attorneys for Applicant